

FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT				ATTY. DOCKET NO. 1875.1060002		APPLICATION NO. To Be Assigned 1875 10/781 822	
				FIRST NAMED INVENTOR Ka Lun Chol			
				FILING DATE February 20, 2004		ART UNIT To Be Assigned	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
<i>Kan</i> ↓	AA1	5,382,917	01/1995	Miyake et al.			
	AB1	5,901,350	05/1999	Stolchita et al.			
	AC1	5,859,559	01/1999	Hong et al.			
	AD1	3,564,439	02/1971	Rao			
	AE1	3,641,450	02/1972	Lunn			
	AF1	5,625,307	04/1997	Scheinberg			
	AG1	5,668,502	09/1997	Rijns			
	AH1	5,828,265	10/1998	Mensink et al.			
	AI1	6,509,796 B2	01/2003	Nguyen et al.			
	AJ1	2003/0128071 A1	07/10/2003	Nguyen et al.			
	AK1						
FOREIGN PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
<i>Kan</i> ↓	AL1	0 583 586 A1	02/1994	EP			Yes No
	AM1	0 587 965 A1	03/1994	EP			Y s No
	AN1						Yes No
	AO1						Yes No
	AP1						Y s No
OTHER (Including Author, Title, Date, Pertinent Pages, etc.)							
<i>Kan</i>	AR	1	Furth, P.M. and Andreou, A.G., "Linearised differential transconductors in subthreshold CMOS," Electronics Letters, Vol. 31, No. 7, pp. 545-547 (March 30, 1995).				
<i>Kan</i>	AS	1	Mensink, C.H.J. et al., "A CMOS "Soft-Switched" Transconductor and Its Application in Gain Control and Filters," IEEE Journal of Solid-State Circuits, Vol. 32, No. 7, IEEE, pp. 989-998 (July 1997).				
	AT	1					
EXAMINER WELLS					DATE CONSIDERED 7/9/04		

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.